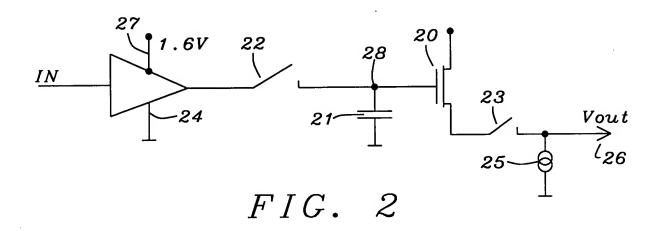


FIG. 1 - Prior Art



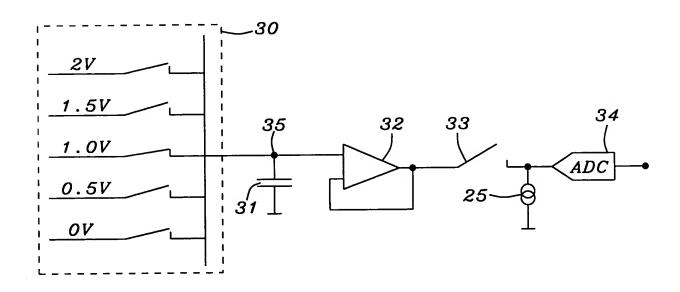


FIG. 3

## Start

Deploy a capacitor as DRAM storage element

41

42

Deploy a transistor, having a minimal threshold voltage, to sense the charge of said storage capacitor. Said charge represents a value of stored information

## FIG. 4

## Start

Mask the channel of the natural transistor to avoid any impurities caused by the following ion implant stop

Perform ion implant to define threshold voltage of the standard transistors

Remove mask from natural transistor and continue standard processes

FIG. 5